

**Notic of References Cit d**

Application/Control No.

10/056,287

Applicant(s)/Patent Under  
Reexamination  
WEST, BURNELL G.

Examiner

John H Le

Art Unit

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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Boerker, "Data reception circuit", Pub. No.: US 2003/0035502, PubDate: February 20, 2003, Foreign Application Priority filed on January 15, 2001.
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)

Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.